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1030 SW MORRISON STREET
PORTLAND, OREGON 97205 USA

(503) 222-3613
FAX: (503) 274-4622

Internet: www.techlaw.com

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FAX: 703-746-9195

FROM: Alan T. McCollom, Reg. No. 28,881

Date: December 31, 2003

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REMARKS:

Re: U.S. Application No. 10/636,131
Our Docket No. 8750-044

Enclosed is a Request for a Corrected Filing Receipt.

NOTICE

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PATENT APPLICATION
Docket No. 8750-044
Client Ref. No. SPX200208-018US

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent application of: Mun-Mo Jeong, et al.

Serial No. 10/636,131 Group No. 2811

Filed: August 6, 2003 Conf. No. 2385

Title: BITLINE OF SEMICONDUCTOR DEVICE HAVING STUD TYPE
CAPPING LAYER AND METHOD FOR FABRICATING THE SAME

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

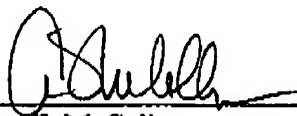
REQUEST FOR CORRECTION OF FILING RECEIPT

The Filing Receipt (Exhibit A) for the above-identified application shows the priority information as claiming priority from Republic of Korea Application No. 2002-505246, filed on August 23, 2002.

The certified copy of the priority application (Exhibit B) shows the Korean Application No. as 2002-50246, filed on August 23, 2002.

Applicant requests correction of the Filing Receipt to correct the foreign priority information.

Respectfully submitted,
MARGER JOHNSON & McCOLLOM, P.C.


Alan F. McCollom
Reg. No. 28,881

MARGER JOHNSON & McCOLLOM, P.C.
1030 SW Morrison Street
Portland, OR 97205
(503) 222-3613

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APPL NO.	FILING OR 371 (c) DATE	ART UNIT	FIL FEE REC'D	ATTY, DOCKET NO	DRAWINGS	TOT CLMS	IND CLMS
10/636,131	08/06/2003	2811	750	8750-044	26	20	3

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1030 S.W. Morrison Street
Portland, OR 97205

CONFIRMATION NO. 2385

UPDATED FILING RECEIPT

OC000000011509290

Date Mailed: 12/17/2003

Receipt is acknowledged of this regular Patent Application. It will be considered in its order and you will be notified as to the results of the examination. Be sure to provide the U.S. APPLICATION NUMBER, FILING DATE, NAME OF APPLICANT, and TITLE OF INVENTION when inquiring about this application. Fees transmitted by check or draft are subject to collection. Please verify the accuracy of the data presented on this receipt. If an error is noted on this Filing Receipt, please write to the Office of Initial Patent Examination's Filing Receipt Corrections, facsimile number 703-746-9195. Please provide a copy of this Filing Receipt with the changes noted thereon. If you received a "Notice to File Missing Parts" for this application, please submit any corrections to this Filing Receipt with your reply to the Notice. When the USPTO processes the reply to the Notice, the USPTO will generate another Filing Receipt incorporating the requested corrections (if appropriate).

Applicant(s)

Mun-Mo Jeong, Seoul, KOREA, REPUBLIC OF;
Chang-Huhn Lee, Gyeonggi-do, KOREA, REPUBLIC OF;
Makoto Yoshida, Gyeonggi-do, KOREA, REPUBLIC OF;

Domestic Priority data as claimed by applicant

Foreign Applications

REPUBLIC OF KOREA 2002-⁵⁰²⁴⁶~~505246~~ 08/23/2002

If Required, Foreign Filing License Granted: 11/04/2003

Projected Publication Date: 03/25/2004

Non-Publication Request: No

Early Publication Request: No

Title

Bitline of semiconductor device having stud type capping layer and method for fabricating the same

Pr liminary Class

bit + A

257

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Title 37, Code of Federal Regulations, 5.11 & 5.15**

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This is to certify that the following application annexed hereto
is a true copy from the records of the Korean Intellectual
Property Office.

출원번호 : 10-2002-0050246
Application Number PATENT-2002-0050246

출원년월일 : 2002년 08월 23일
Date of Application AUG 23, 2002

출원인 : 삼성전자 주식회사
Applicant(s) SAMSUNG ELECTRONICS CO., LTD.



2002 년 12 월 04 일

특 허 청

COMMISSIONER

